

#### Co-chairs

María Alpuente (U. P. Valencia, Spain)  
Byron Cook (Microsoft Research, UK)

#### Program Committee

Hassan Ait-Kaci (Ilog, Canada)  
María Alpuente (U. P. Valencia, Spain)  
Thomas Arts (IT Goteborg, Sweden)  
Demis Ballis (Univ. Udine, Italy)  
Josh Berdine (Microsoft Research, UK)  
Lubos Brim (U. Masaryk, Czech Republic)  
Darren Cofer (Rockwell Collins, USA)  
Byron Cook (Microsoft Research, UK)  
Patrick Cousot (ENS, France)  
Santiago Escobar (U. P. Valencia, Spain)  
Azadeh Farzan (Univ. Toronto, Canada)  
Hubert Garavel (INRIA, France)  
Stefania Gnesi (ISTI-CNR, Italy)  
Alexey Gotsman (Univ. Cambridge, UK)  
Holger Hermanns (U.Saarland, Germany)  
Christophe Joubert (U. P. Valencia, Spain)  
Daniel Kroening (ETHZ, Switzerland)  
Michael Leuschel (Dusseldorf, Germany)  
Pedro Merino (Univ. Malaga, Spain)  
Juan Jose Moreno-Navarro (UPM, Spain)  
Corina Pasareanu (NASA, USA)  
Jaco v.d. Pol (U.Twente, The Netherlands)  
Murali Rangarajan (Honeywell, USA)  
Jakob Rehof (Univ. Dortmund, Germany)  
Andrey Rybalchenko (MPI, Germany)  
Marcel Verhoef (Chess, The Netherlands)  
Martin Wirsing (U. Munchen, Germany)  
Hongseok Yang (Univ. London, UK)  
Greta Yorsh (IBM, USA)

#### ERCIM FMICS WG Coordinator

Alessandro Fantechi (Univ. Firenze, Italy)

#### Workshop chair

Christophe Joubert (U. P. Valencia, Spain)

#### Invited Speakers

Dino Distefano (Queen Mary, UK)  
Diego Latella (ISTI-CNR, Italy)  
Thierry Lecomte (ClearSy, France)  
Ken McMillan (Cadence, USA)

#### Important Dates

Deadline for abstracts: 16 April  
Deadline for papers: 20 April  
Accept/Reject Notification: 15 June  
Camera-ready version: 15 July  
Workshop: 2-3 November 2009

#### Sponsored by:



#### Supported by:



Published by:  Springer

#### Scope of the workshop

The aim of the FMICS workshop series is to provide a forum for researchers who are interested in the development and application of formal methods in industry. In particular, these workshops bring together scientists and engineers that are active in the area of formal methods and interested in exchanging their experiences in the industrial usage of these methods. These workshops also strive to promote research and development for the improvement of formal methods and tools for industrial applications. Topics include, but are not restricted to:

- Design, specification, code generation and testing based on formal methods.
- Methods, techniques and tools to support automated analysis, certification, debugging, learning, optimization and transformation of complex, distributed, real-time systems and embedded systems.
- Verification and validation methods that address shortcomings of existing methods with respect to their industrial applicability (e.g., scalability and usability issues).
- Tools for the development of formal design descriptions.
- Case studies and experience reports on industrial applications of formal methods, focusing on lessons learned or identification of new research directions.
- Impact of the adoption of formal methods on the development process and associated costs.
- Application of formal methods in standardization and industrial forums.

#### Paper submissions and publication

Submissions must be made electronically through the [EasyChair system](#).

Papers should be up to 16 pages in LNCS format, with the names and affiliations of the authors and a clear and informative abstract. Additional details may be included in a clearly marked appendix, which will be read at the discretion of the program committee. All submissions must report on original research.

Submitted papers must not have previously appeared in a journal or conference with published proceedings and must not be concurrently submitted to any other peer-reviewed workshop, symposium, conference or archival journal. Any partial overlap with any such published or concurrently submitted paper must be clearly indicated.

Case study papers should identify lessons learned, validate theoretical results (such as scalability of methods), or provide specific motivation for further research and development.

The workshop proceedings will be published in the Springer series Lecture Notes in Computer Science (LNCS).